## Differential Equations

 Class NotesWe will study the diff. eqs. concerning two simple electrical

Electrical Circuits (Section 3.5)
A simple electrical circuit has a voltage source (like a battery or generator), a resistor, and either a capacitor or an inductor. Let's define some terms and see how these circuits generate diff. eqs.

One of the main differences between a capacitor and an inductor is that a capacitor opposes a change in voltage while an inductor opposes a change in the current. Furthermore, the inductor stores energy in the form of a magnetic field, and the capacitor stores energy in the form of an electric field. (source: https://byjus.com/physics/difference-between-capacitor-and-inductor/)

Definition: Inductance: The property of an electric circuit by which an electromotive force (voltage) is induced in it by a variation of current either in the circuit itself or in a neighboring circuit. An inductor is a coil of wire wrapped around an iron core.
(source: https://www.swtc.edu/Ag_Power/electrical/terms.htm)
Definition: Voltage (volts, V): Voltage is the force that makes electrons flow. It's a difference in potential energy between two different points in a circuit.

Definition: Current (amperes or amps, A): Current is the rate of the flow of electrons.
Definition: Power (Watts, W or w): The power used in a circuit is measured in watts. Watts are calculated by multiplying the voltage by the current.

Definition: Resistance: This is the measure of how well something conducts electricity. If it has a low resistance, the object is a great conductor of electricity. If it has a high resistance, that means that it doesn't conduct electricity well. A resistor is a device that resists current flow. (above definitions source: https://dewesoft.com/blog/volts-and-currents-explained)

Definition: Capacitance: Capacitance is the capability of a material object or device to store electric charge. It is measured by the charge in response to a difference in electric potential, expressed as the ratio of those quantities. (source: www.wikipedia.org)


## The Physics Behind the Math:

The story goes that good, old Gustav Robert Kirchhoff in 1859 came up with these gems.

1. Kirchhoff's Current Law: The algebraic sum of the currents flowing into any junction point must be zero.
2. Kirchhoff's Voltage Law: The algebraic sum of the instantaneous changes in potential (voltage drops) around any closed loop must be zero.

Here are pictures of the two circuits we will be exploring.

(a)

(b)


Figure 3.13 (a) $R L$ circuit and (b) $R C$ circuit

To apply Kirchhoff's voltage law, we need to know the voltage drop across each element of the circuit. We state them here.

1. According to Ohm's Law, the voltage drop $E_{R}$ across a resistor is proportional to the current $I$ passing through the resistor. The proportionality constant $R$ is called the resistance.

$$
E_{R}=R I \text { where } R \text { is a real number }
$$

2. Faraday's Law and Lenz's Law can be used to show that the voltage drop $E_{L}$ across an inductor is proportional to the instantaneous rate of change of the current $I$. The proportionality constant $L$ is called the inductance.

$$
\mathrm{E}_{\mathrm{L}}=\mathrm{L} \frac{\mathrm{dI}}{\mathrm{dt}} \text { where } L \text { is a real number }
$$

3. The voltage drop $E_{C}$ across a capacitor is proportional to the electrical charge $q$ on the capacitor. The constant $C$ is called the capacitance.

$$
E_{C}=\frac{1}{C} q \text { where } C \text { is a real number }
$$

## Rationale and Method for RL Circuits:

A voltage source is assumed to add voltage (or potential energy) to a circuit. Let $E(t)$ be the voltage supplied by a circuit at time $t$. We now apply Kirchhoff's Voltage Law to the RL (resistance, inductance) circuit shown on the previous page. This gets us
$\mathrm{E}_{\mathrm{L}}+\mathrm{E}_{\mathrm{R}}=\mathrm{E}(\mathrm{t})$
$\mathrm{L} \frac{\mathrm{dI}}{\mathrm{dt}}+R I=\mathrm{E}(\mathrm{t})$
$\frac{\mathrm{dI}}{\mathrm{dt}}+\frac{R}{\mathrm{~L}} I=\frac{\mathrm{E}(\mathrm{t})}{\mathrm{L}}$


We know that the solution to the general linear equation is $y=\frac{\int \mu(x) Q(x) d x+c}{\mu(x)}$. Hence, the solution to this circuit's diff. eq. is $I(t)=e^{-R t / L}\left[\int e^{R t / L} \cdot \frac{E(t)}{L} d t+K\right]$. Here, $K$ is the constant of integration (a real number).


There is room to solve on the next page.


Perform the integration and simplify to find $I(t)$ with a constant of integration in place. Then use the initial point given to find the constant. Do not forget to find the voltage which is $\mathrm{E}_{\mathrm{L}}=\mathrm{L} \frac{\mathrm{dI}}{\mathrm{dt}}$.

## Rationale and Method for RC Circuits:

A voltage source is assumed to add voltage (or potential energy) to a circuit. Let $E(t)$ be the voltage supplied by a circuit at time $t$. We now apply Kirchhoff's Voltage Law to the RC (resistance, capacitance) circuit shown here. This gets us

$\frac{\mathrm{dq}}{\mathrm{dt}}+\frac{1}{R C} \mathrm{q}=\frac{\mathrm{E}(\mathrm{t})}{\mathrm{R}} \quad \circ \bigcirc$


We solve this with our method for linear equations (using $\mu(t)=e^{\int \frac{1}{R C} d t}=e^{\frac{t}{R C}}$ ) to get us $q(t)=e^{\frac{-t}{R C}} \cdot \frac{1}{R} \int e^{\frac{t}{R C}} \cdot E(t) d t$. Use $K$ for the constant of integration (a real number).
expl 2: Assume $E(t)$ is constant at $V$ volts. Solve for $q(t)$, the capacitor charge for $t>0$. Recall, the resistance is $R$ Ohms and the capacitance is $C$ farads. Further, assume $q(0)=Q$ coulombs and solve for the constant of integration. I have substituted $V$ in for $E(t)$ below.
$q(t)=e^{\frac{-t}{R C}} \cdot \frac{1}{R} \int e^{\frac{t}{R C}} \cdot V d t$
expl 3: The pathway for a binary electrical signal between gates in an integrated circuit can be modeled as an RC circuit. The voltage source models the transmitting gate and the capacitor models the receiving gate. Typically, the resistance is 100 Ohms and the capacitance is very small at $10^{-12}$ farads. If the capacitor is initially uncharged $(Q=0)$ and the transmitting gate changes instantaneously from 0 to 5 volts, how long will it take for the voltage at the receiving gate to reach 3 volts? (This is the time it takes to transmit a logical " 1 ".)
(derived in example 2)
$q(t)=V C+(Q-V C) e^{-t / R C}$


